



Attorney Docket No.: 0553-0401

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Yoshifumi TANADA )  
Serial No.: 10/807,692 )  
Filed: March 24, 2004 )  
For: Circuit For Inspecting Semiconductor )  
Device And Inspecting Method )  
Examiner: )  
Art Unit: )

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

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Shannon Wallace  
Date: August 31, 2004

PRELIMINARY AMENDMENT A

Prior to examination and calculation of fees, please amend the above-identified application as follows: